

	Hits	Search Text	DBs
1	125	((reference delay adj line) with (reference adj clock))) same (((feedback delay adj line) with (feedback adj clock)))	USPAT; US-PGPUB
2	24	(((reference delay adj line) with (reference adj clock))) same (((feedback delay adj line) with (feedback adj clock)))) same (programmable delay adj line)	USPAT; US-PGPUB
3	10	((((reference delay adj line) with (reference adj clock))) same (((feedback delay adj line) with (feedback adj clock)))) same (programmable delay adj line)) same (phase adj detector)	USPAT; US-PGPUB
4	3	((reference delay adj line) with (reference adj clock))) same (((feedback delay adj line) with (feedback adj clock))) same (divide adj2 two)	USPAT; US-PGPUB
5	18	((reference delay adj line) with (reference adj clock))) same (((feedback delay adj line) with (feedback adj clock))) same (frequency adj divider)	USPAT; US-PGPUB
6	4	((reference delay adj line) with (reference adj clock))) same (((feedback delay adj line) with (feedback adj clock))) same (frequency adj divider) same (phase adj detector)	USPAT; US-PGPUB
7	13	dll with fpga	USPAT; US-PGPUB
8	8	(delay\$2 adj lock\$2 adj loop) with fpga	USPAT; US-PGPUB
9	16	(dll with fpga) or ((delay\$2 adj lock\$2 adj loop) with fpga)	USPAT; US-PGPUB
10	5	(dll with fpga) and ((delay\$2 adj lock\$2 adj loop) with fpga)	USPAT; US-PGPUB
11	335	programmable adj delay adj line	USPAT; US-PGPUB
12	7	(programmable adj delay adj line) same (dll or (delay\$2 adj lock\$2 adj loop))	USPAT; US-PGPUB
13	21	(clock adj tree) same (dll or (delay\$2 adj lock\$2 adj loop))	USPAT; US-PGPUB
14	5	(clock adj doubler) same (dll or (delay\$2 adj lock\$2 adj loop))	USPAT; US-PGPUB
15	16	(clock adj doubler) and (dll or (delay\$2 adj lock\$2 adj loop))	USPAT; US-PGPUB

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	Hits	Search Text	DBs
16	1	deskew with (clock adj2 out)	USPAT; US_PGPUB
17	3	(deskew\$3 adj3 clock) same (clock adj2 out)	USPAT; US_PGPUB
18	0	(deskew\$3 adj3 clock) same (("0" or zero) adj ns)	USPAT; US_PGPUB
19	27	("0 ns") with (clock) with reference	USPAT; US_PGPUB
20	14	programmable adj (dll or (delay adj2 loop))	USPAT; US_PGPUB
21	1505	(external adj clock) near4 (internal adj clock)	USPAT; US_PGPUB
22	97	select\$4 near4 ((external adj clock) near4 (internal adj clock))	USPAT; US_PGPUB
23	72	"one" adj3 ((external adj clock) near4 (internal adj clock))	USPAT; US_PGPUB
24	32	"one" adj ((external adj clock) near4 (internal adj clock))	USPAT; US_PGPUB
25	0	("one" adj ((external adj clock) near4 (internal adj clock))) same (dll or (delay adj lock\$2 adj loop))	USPAT; US_PGPUB
26	3	(select\$4 near4 ((external adj clock) near4 (internal adj clock))) same (dll or (delay adj lock\$2 adj loop))	USPAT; US_PGPUB
27	2	("one" adj ((external adj clock) near4 (internal adj clock))) and (dll or (delay adj lock\$2 adj loop))	USPAT; US_PGPUB
28	115	((external adj clock) near4 (internal adj clock)) with reference	USPAT; US_PGPUB
29	57	((external adj clock) near4 (internal adj clock)) with reference) and (dll or (delay adj lock\$2 adj loop))	USPAT; US_PGPUB
30	1	((external adj clock) near4 (internal adj clock)) with reference) and (programmable with (dll or (delay adj lock\$2 adj loop)))	USPAT; US_PGPUB
31	32	(select\$4 near2 coupl\$3) same (dll or (delay adj lock\$2 adj loop))	USPAT; US_PGPUB
32	2	(programmably adj coupl\$3) same (dll or (delay adj lock\$2 adj loop))	USPAT; US_PGPUB
33	2	(programmably adj select\$3) same (dll or (delay adj lock\$2 adj loop))	USPAT; US_PGPUB
34	2	(programmable) same (dll or (delay adj lock\$2 adj loop))	EPO; JPO
35	19	(programmable) same (dll or (delay adj lock\$2 adj loop))	DERWENT; IBM_TDB

	Hits	Search Text	DBs
36	8	(input adj select\$3) with (dll or (delay adj lock\$2 adj loop))	USPAT; US-PGPUB
37	28	(input adj2 select\$3) with (dll or (delay adj lock\$2 adj loop))	USPAT; US-PGPUB
38	5	(input near2 select\$3 near2 clock) with (dll or (delay adj lock\$2 adj loop))	USPAT; US-PGPUB
39	0	((programmabl\$5 adj delay) same (feedback adj delay)) and ((clock near2 doubl\$3) same (clock adj tree))	USPAT; US-PGPUB
40	0	((programmabl\$5 adj delay) same (feedback adj delay)) and ((clock near2 doubl\$3) and (clock adj tree))	USPAT; US-PGPUB
41	0	((programmabl\$5) same (feedback adj delay)) and ((clock near2 doubl\$3) same (clock adj tree))	USPAT; US-PGPUB
42	0	((programmabl\$5) same (feedback adj delay)) and ((clock near2 doubl\$3) and (clock adj tree))	USPAT; US-PGPUB
43	0	((programmabl\$5) and (feedback adj delay)) and ((clock near2 doubl\$3) and (clock adj tree))	USPAT; US-PGPUB
44	0	((programmabl\$5 adj delay) same (feedback)) and ((clock near2 doubl\$3) and (clock adj tree))	USPAT; US-PGPUB
45	33	((clock near2 doubl\$3) and (clock adj tree))	USPAT; US-PGPUB
46	13	(((clock near2 doubl\$3) and (clock adj tree))) and programmabl\$3	USPAT; US-PGPUB
47	8	((clock near2 doubl\$3) and (clock adj tree)) and fpga	USPAT; US-PGPUB
48	0	((programmabl\$5 adj delay) same (feedback adj delay)) and fpga	USPAT; US-PGPUB
49	4	((programmabl\$5 adj delay) same (feedback adj delay))	USPAT; US-PGPUB
50	118	((programmabl\$5 adj delay) same (feedback))	USPAT; US-PGPUB
51	9	((programmabl\$5 adj delay) same (clock near2 doubl\$3))	USPAT; US-PGPUB
52	48	(((reference delay adj line) with (reference adj clock))) same (((feedback delay adj line) with (feedback adj clock))) same (programmable delay adj line)	USPAT; US-PGPUB
53	24	(programmable adj delay adj line) same (dll or (delay\$2 adj lock\$2 adj loop))	USPAT; US-PGPUB
54	18	(((clock near2 doubl\$3) and (clock adj tree))) and programmabl\$3	USPAT; US-PGPUB

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	Hits	Search Text	DBs
55	0	((reference delay adj line) with (reference adj clock))) same (((feedback delay adj line) with (feedback adj clock))) same (programmable delay adj line)	EPO; JPO
56	1	(programmable adj delay adj line) same (dll or (delay\$2 adj lock\$2 adj loop))	EPO; JPO
57	0	((clock near2 doubl\$3) and (clock adj tree))) and programmabl\$3	EPO; JPO

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